**Microprocessor Systems and Interfacing (EEE342)**

**Bachelors of Science in Electrical (Telecommunication) Engineering FA12**

**Quiz No. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ 6th Semester (SP15) Section: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

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| **Name** |  |
| **Registration No.** |  |
| **Date** |  |

**Total Marks: Five Time: 10 Minutes**

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**Question:**

Draw and label the complete schematics to interface a 16KB SRAM with 8088 CPU. Memory map for SRAM starts from C8000H and above. Also provide the memory map for this SRAM.

**Solution:**